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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Application Number	10/607,218
				Filing Date	June 27, 2003
				First Named Inventor	Michio YAMASHITA
				Art Unit	2116
				Examiner Name	Anand B. PATEL
Sheet	1	of	2	Attorney Docket Number	04329.3085

U.S. PATENTS AND PUBLISHED U.S. PATENT APPLICATIONS					
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ABP		CHRISTOPHER J. HUGHES et al., Saving Energy with Architectural and Frequency Adaptations for Multimedia Applications, Conference Proceedings Article, December 1, 2001, pages 250-260, Proceedings. 34 th ACM/IEEE International Symposium December 1-5, 2001, Piscataway, NJ.	
ABP		KINSHUK GOVIL et al., Comparing Algorithms for Dynamic Speed-Setting of a Low-Power CPU, available at http://citeseer.ist.psu.edu/govil95comparing.html .	
ABP		Communication pursuant to Article 96(2) EPC mailed April 24, 2006, from European Patent Office in European Patent Application No. 03014280.6-2006.	
ABP		Minutes of Telephone Conversation of April 28, 2006, from European Patent Office in European Patent Application No. 03014280.6-2006.	

Examiner Signature	/Anand Patel/	Date Considered	10/05/2006
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